

FEATURES

- Meets or Exceeds the Requirements of TIA/EIA-232-F and ITU v.28 Standards
- Operates With 3-V to 5.5-V V_{CC} Supply •
- **Operates at Least 1 Mbit/s** •
- Low Standby Current . . . 1 µA Typ •
- External Capacitors . . . $4 \times 0.1 \ \mu F$ •
- Accepts 5-V Logic Input With 3.3-V Supply
- Designed to Be Interchangeable With Maxim[™] • **MAX3227E**
- Latch-Up Performance Exceeds 100 mA Per JESD 78. Class II
- ESD Protection for RS-232 I/O Pins
 - ±15 kV Human-Body Model
 - ±8 kV IEC61000-4-2, Contact Discharge
 - ±15 kV IEC61000-4-2, Air-Gap Discharge
- Auto-Powerdown Plus Feature Automatically **Disables Drivers for Power Savings**
- Packaged in Plastic Shrink Small-Outline Package

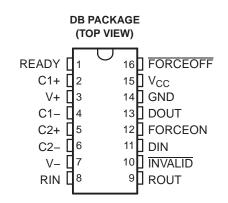
DESCRIPTION/ORDERING INFORMATION

APPLICATIONS

Battery-Powered, Hand-Held, and Portable Equipment

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- **PDAs and Palmtop PCs**
- Notebooks, Sub-Notebooks, and Laptops .
- **Digital Cameras**
- **Mobile Phones and Wireless Devices**



The MAX3227E consists of one line driver, one line receiver, and a dual charge-pump circuit with ±15-kV IEC ESD protection pin to pin (serial-port connection pins, including GND). The device meets the requirements of TIA/EIA-232-F and provides the electrical interface between an asynchronous communication controller and the serial-port connector. The charge pump and four small external capacitors allow operation from a single 3-V to 5.5-V supply. This device operates at data-signaling rates of 1 Mbit/s in normal operating mode and a maximum of 30-V/µs driver output slew rate. This device also features a logic-level output (READY) that asserts when the charge pump is regulating and the device is ready to begin transmitting.

The MAX3227E achieves a 1-µA supply current using the auto-powerdown plus feature. This device automatically enters a low-power powerdown mode when the RS-232 cable is disconnected or the drivers of the connected peripherals are inactive for more than 30 s. They turn on again when they sense a valid transition at any driver or receiver input. Auto-powerdown saves power without changes to the existing BIOS or operating system.

The MAX3227EC is characterized for operation from 0°C to 70°C. The MAX3227EI is characterized for operation from -40°C to 85°C.

T _A	PAC	PACKAGE ⁽¹⁾⁽²⁾ ORDERABLE PART NUMBER		TOP-SIDE MARKING
000 to 7000		Tube of 80	MAX3227ECDB	MD007E0
0°C to 70°C	SSOP – DB	Reel of 2000	MAX3227ECDBR	- MP227EC
40%C to 95%C	SSOP – DB	Tube of 80	MAX3227EIDB	MD007EL
–40°C to 85°C	330P - DB	Reel of 2000	MAX3227EIDBR	

ORDERING INFORMATION

(1) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI (2)website at www.ti.com.



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MAX3227E 3-V TO 5.5-V SINGLE-CHANNEL RS-232 LINE DRIVER/RECEIVER WITH $\pm 15\text{-kV}$ IEC ESD PROTECTION



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			F	UNCTIO	N TABLE ⁽¹⁾)		
	INPUT CO	NDITIONS			OUTPUT	STATES		
FORCEON	FORCEOFF	RECEIVER OR DRIVER EDGE WITHIN 30 s	VALID RS-232 LEVEL PRESENT AT RECEIVER	DRIVER	RECEIVER	INVALID	READY	OPERATING MODE
			Auto-	Powerdow	n Plus Condit	ions		
н	н	NO	NO	Active	Active	L	н	Normal operation, auto-powerdown plus disabled
Н	н	NO	YES	Active	Active	н	Н	Normal operation, auto-powerdown plus disabled
L	н	YES	NO	Active	Active	L	Н	Normal operation, auto-powerdown plus enabled
L	н	YES	YES	Active	Active	н	Н	Normal operation, auto-powerdown plus enabled
L	н	NO	NO	Z	Active	L	L	Powerdown, auto-powerdown plus enabled
L	н	NO	YES	Z	Active	н	L	Powerdown, auto-powerdown plus enabled
Х	L	Х	NO	Z	Active	L	L	Manual powerdown
Х	L	Х	YES	Z	Active	Н	L	Manual powerdown
			Au	to-Powerd	own Conditio	ns		
INVALID	INVALID	Х	NO	Z	Active	L	L	Powerdown, auto-powerdown enabled
INVALID	INVALID	Х	YES	Active	Active	Н	Н	Normal operation, auto-powerdown enabled

(1) H = high level, L = low level, X = irrelevant, Z = high impedance

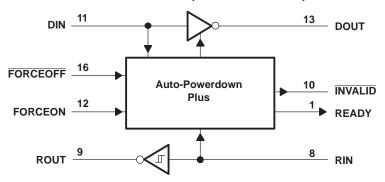
$$3$-V$ TO 5.5-V SINGLE-CHANNEL RS-232 LINE DRIVER/RECEIVER WITH <math display="inline">\pm 15\text{-kV}$ IEC ESD PROTECTION

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TERMINAL FUNCTIONS

TERMINA	۹L	DECODIDION
NAME	NO.	DESCRIPTION
C1+	2	Positive terminal of voltage-doubler charge-pump capacitor
C1-	4	Negative terminal of voltage-doubler charge-pump capacitor
C2+	5	Positive terminal of inverting charge-pump capacitor
C2-	6	Negative terminal of inverting charge-pump capacitor
DIN	11	CMOS driver input
DOUT	13	RS-232 driver output
FORCEOFF	16	Force-off input, active low. Drive low to shut down drivers, receivers, and charge pump. This overrides auto-shutdown and FORCEON (see Function Table).
FORCEON	12	Force-on input, active high. Drive high to override powerdown, keeping drivers and receivers on (FORCEOFF must be high) (see Function Table).
GND	14	Ground
INVALID	10	Valid signal detector output, active low. A logic high indicates that a valid RS-232 level is present on a receiver input.
READY	1	Ready to transmit output, active high. READY is enabled high when V– goes below –3.5 V and the device is ready to transmit.
RIN	8	RS-232 receiver input
ROUT	9	CMOS receiver output
V+	3	$+2 \times V_{CC}$ generated by the charge pump
V–	7	$-2 \times V_{CC}$ generated by the charge pump
V _{CC}	15	3-V to 5.5-V single-supply voltage

LOGIC DIAGRAM (POSITIVE LOGIC)



MAX3227E 3-V TO 5.5-V SINGLE-CHANNEL RS-232 LINE DRIVER/RECEIVER WITH ±15-kV IEC ESD PROTECTION



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Absolute Maximum Ratings⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT	
V_{CC}	Supply voltage range ⁽²⁾		-0.3	6	V	
V+	Positive output supply voltage range ⁽²⁾		-0.3	7	V	
V–	legative output supply voltage range ⁽²⁾		0.3	-7	V	
V + - V -	upply voltage difference ⁽²⁾			13	V	
V		Driver (FORCEOFF, FORCEON)	-0.3	6	V	
VI	Input voltage range	Receiver	-25	25	v	
N		Driver	-13.2	13.2	V	
Vo	Output voltage range	Receiver (INVALID, READY)	-0.3	V _{CC} + 0.3	v	
	Short-circuit duration	DOUT to GND		Unlimited		
θ_{JA}	Package thermal impedance ⁽³⁾			82	°C/W	
	Lead temperature 1,6 mm (1/16 in) from case for 10 s			260	°C	
T _{stg}	Storage temperature range		-65	150	°C	

(1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

All voltages are with respect to network GND. (2)

(3) The package thermal impedance is calculated in accordance with JESD 51-7.

Recommended Operating Conditions⁽¹⁾

See Figure 5

				MIN	NOM	MAX	UNIT
	Supply voltage		$V_{CC} = 3.3 V$	3	3.3	3.6	V
	Supply voltage		$V_{CC} = 5 V$	4.5	5	5.5	v
V _{IH}	Driver and control high level input voltage	DIN, FORCEOFF, FORCEON	$V_{CC} = 3.3 V$	2		5.5	V
	Driver and control high-level input voltage	DIN, FORCEOFF, FORCEON	$V_{CC} = 5 V$	2.4		5.5	
V_{IL}	Driver and control low-level input voltage	DIN, FORCEOFF, FORCEON		0		0.8	V
VI	Receiver input voltage			-25		25	V
т	Operating free air temperature		MAX3227EC	0		70	°C
T _A	Operating free-air temperature		MAX3227EI	-40		85	

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V ± 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V ± 0.5 V.

Electrical Characteristics⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 5)

	PARA	METER	TEST CONDITIONS	MIN	TYP ⁽²⁾	MAX	UNIT
II -	Input leakage current	FORCEOFF, FORCEON			±0.01	±1	μA
		Auto-powerdown plus disabled	No load, FORCEON at V_{CC}		0.3	2	mA
Icc	Supply current	Powered off	No load, FORCEOFF at GND		1	±1	
	(T _A = 25°C)	Auto-powerdown plus enabled	No load, FORCEOFF at V _{CC} , FORCEON at GND, All RIN are open or grounded		1	10	μA

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V ± 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V ± 0.5 V. (2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C.

DRIVER SECTION

Electrical Characteristics⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 1 and Figure 2)

	PARAMETER	TEST C	ONDITIONS		MIN	TYP ⁽²⁾	MAX	UNIT	
V _{OH}	High-level output voltage	DOUT at $R_L = 3 k\Omega$ to GND,	DIN = GND		5	5.4		V	
V _{OL}	Low-level output voltage	DOUT at $R_L = 3 k\Omega$ to GND,	$DIN = V_{CC}$		-5	-5.4		V	
I _{IH}	High-level input current	$V_{I} = V_{CC}$				±0.01	±1	μA	
I _{IL}	Low-level input current	V _I at GND				±0.01	±1	μA	
	Short-circuit output current ⁽³⁾	V _{CC} = 3.6 V,	$V_{O} = 0 V$			±35	±60	A	
I _{OS}	Short-circuit output current.	V _{CC} = 5.5 V,	$V_{O} = 0 V$			±35	±60	mA	
r _o	Output resistance	V_{CC} , V+, and V- = 0 V,	$V_0 = \pm 2 V$		300	10M		Ω	
I _{off}	Output leakage current	FORCEOFF = GND,	$V_0 = \pm 12 V$,	$V_{CC} = 0$ to 5.5 V			±25	μA	

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V ± 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V ± 0.5 V.

(2)

All typical values are at $V_{CC} = 3.3$ V or $V_{CC} = 5$ V, and $T_A = 25^{\circ}$ C. Short-circuit durations should be controlled to prevent exceeding the device absolute power dissipation ratings, and not more than one (3) output should be shorted at a time.

Switching Characteristics⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 1 and Figure 2)

	PARAMETER	T	EST CONDITIONS		MIN	TYP ⁽²⁾	MAX	UNIT
		C _L = 1000 pF, One DIN switching,	R _L = 3 kΩ, See Figure 1		250			
	Maximum data rate	$C_{L} = 1000 \text{ pF},$ $V_{CC} = 4.5 \text{ V},$	R _L = 3 kΩ, See Figure 1	One DIN switching,	1000			kbit/s
		$C_{L} = 250 \text{ pF},$ $V_{CC} = 3 \text{ V},$	R _L = 3 kΩ, See Figure 1	One DIN switching,	1000			
t _{sk(p)}	Pulse skew ⁽³⁾	$C_{L} = 150 \text{ pF to } 2500 \text{ pF},$	$R_L = 3 k\Omega$ to 7 k Ω ,	See Figure 2		25		ns
SR(tr)	Slew rate, transition region	$V_{CC} = 3.3 \text{ V},$ $C_L = 150 \text{ pF to } 1000 \text{ pF},$	$R_L = 3 k\Omega \text{ to } 7 k\Omega$, See Figure 1		24		150	V/µs

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V ± 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V ± 0.5 V. (2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C. (3) Pulse skew is defined as |t_{PLH} - t_{PHL}| of each channel of the same device.

ESD Protection

TERMIN	AL	TEST CONDITIONS	түр	UNIT
NAME	NO.	TEST CONDITIONS	ITP	UNIT
		Human-Body Model	±15	
DOUT	13	Contact Discharge (IEC61000-4-2)	±8	kV
		Air-Gap Discharge (IEC61000-4-2)	±15	

MAX3227E 3-V TO 5.5-V SINGLE-CHANNEL RS-232 LINE DRIVER/RECEIVER WITH ±15-kV IEC ESD PROTECTION

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RECEIVER SECTION

Electrical Characteristics⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 3)

	PARAMETER	TEST CONDITIONS	MIN	TYP ⁽²⁾	MAX	UNIT
V _{OH}	High-level output voltage	I _{OH} = -1 mA	$V_{CC} - 0.6$	$V_{CC} - 0.1$		V
V _{OL}	Low-level output voltage	I _{OL} = 1.6 mA			0.4	V
V _{IT+}	Desitive going input threshold voltage	$V_{CC} = 3.3 V$		1.5	2.4	V
	Positive-going input threshold voltage	$V_{CC} = 5 V$		1.8	2.4	v
v	Negative going input threshold voltage	V _{CC} = 3.3 V	0.6	1.2		V
V _{IT}	Negative-going input threshold voltage	$V_{CC} = 5 V$	0.8	1.5		v
V _{hys}	Input hysteresis (V _{IT+} - V _{IT-})			0.5		V
I _{off}	Output leakage current			±0.05	±10	μA
r _i	Input resistance	$V_1 = \pm 3 \text{ V to } \pm 25 \text{ V}$	3	5	7	kΩ

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V ± 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V ± 0.5 V. (2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C.

Switching Characteristics⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	TYP ⁽²⁾	UNIT
t _{PLH}	Propagation delay time, low- to high-level output	C _L = 150 pF, See Figure 3	150	ns
t _{PHL}	Propagation delay time, high- to low-level output	C _L = 150 pF, See Figure 3	150	ns
t _{sk(p)}	Pulse skew ⁽³⁾	See Figure 3	50	ns

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V ± 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V ± 0.5 V. (2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C. (3) Pulse skew is defined as |t_{PLH} - t_{PHL}| of each channel of the same device.

ESD Protection

TERMI	NAL	TEST CONDITIONS	TYP	UNIT
NAME	NO.	TEST CONDITIONS	ITP	UNIT
		Human-Body Model	±15	
RIN	8	Contact Discharge (IEC61000-4-2)	±8	kV
		Air-Gap Discharge (IEC61000-4-2)	±15	

AUTO-POWERDOWN SECTION

Electrical Characteristics

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 4)

	PARAMETER	TEST CONDITIONS	MIN	MAX	UNIT
V _{T+(valid)}	Receiver input threshold for INVALID high-level output voltage	FORCEON = GND, $\overline{\text{FORCEOFF}} = V_{CC}$		2.7	V
V _{T-(valid)}	Receiver input threshold for INVALID high-level output voltage	FORCEON = GND, $\overline{FORCEOFF} = V_{CC}$	-2.7		V
V _{T(invalid)}	Receiver input threshold for INVALID low-level output voltage	FORCEON = GND, $\overline{FORCEOFF} = V_{CC}$	-0.3	0.3	V
V _{OH}	INVALID, READY output voltage high	$I_{OH} = -1 \text{ mA}, \text{ FORCEON} = \text{GND}, \overline{\text{FORCEOFF}} = V_{CC}$	V _{CC} - 0.6		V
V _{OL}	INVALID, READY output voltage low	I_{OL} = 1.6 mA, FORCEON = GND, FORCEOFF = V_{CC}		0.4	V

Switching Characteristics

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 4)

	PARAMETER		MIN	TYP ⁽¹⁾	MAX	UNIT
t _{INVH}	Propagation delay time, low- to high-level output		1		μs	
t _{INVL}	Propagation delay time, high- to low-level output		30		μs	
t _{WU}	Supply enable time			100		μs
t _{AUTOPRDN}	Driver or receiver edge to driver's shutdown	15	30	60	S	

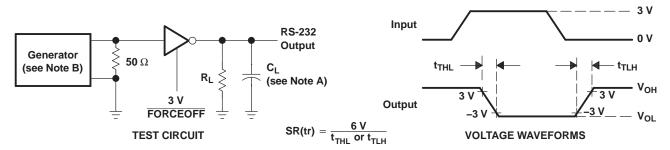
(1) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C.

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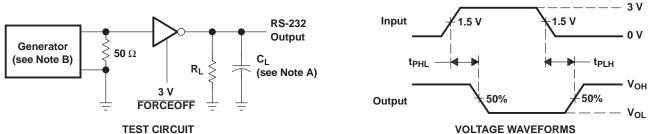
PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: PRR = 250 kbit/s, $Z_0 = 50 \Omega$, 50% duty cycle, $t_r \le 10$ ns, $t_f \le 10$ ns.

Figure 1. Driver Slew Rate



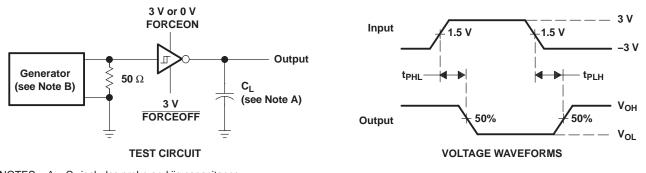
TEST CIRCOTT

VOLIAGE WAVEF

NOTES: A. C_{L} includes probe and jig capacitance.

B. The pulse generator has the following characteristics: PRR = 250 kbit/s, $Z_O = 50 \Omega$, 50% duty cycle, $t_r \le 10$ ns, $t_f \le 10$ ns.

Figure 2. Driver Pulse Skew



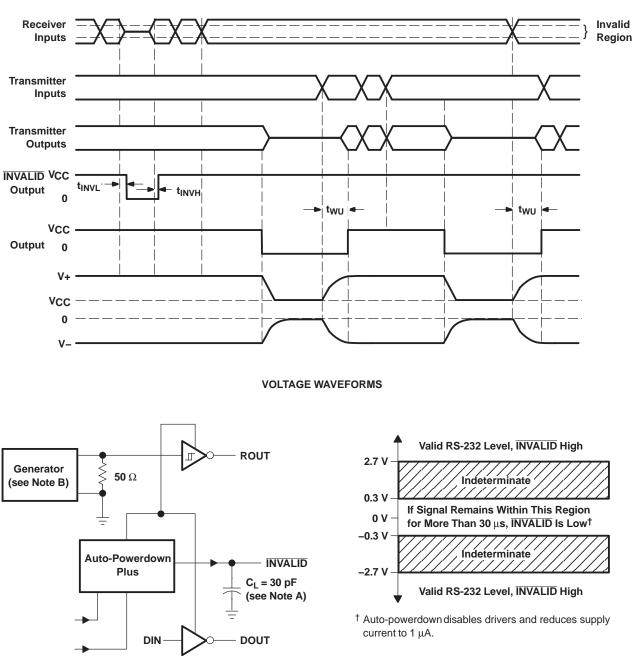
NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: Z_0 = 50 Ω , 50% duty cycle, $t_r \le 10$ ns, $t_f \le 10$ ns.

Figure 3. Receiver Propagation Delay Times

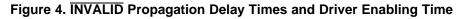
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PARAMETER MEASUREMENT INFORMATION (continued)

TEST CIRCUIT

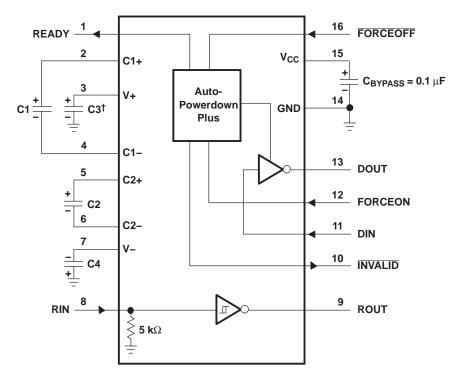


MAX3227E 3-V TO 5.5-V SINGLE-CHANNEL RS-232 LINE DRIVER/RECEIVER WITH $\pm 15\text{-kV}$ IEC ESD PROTECTION



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APPLICATION INFORMATION



^{\dagger} C3 can be connected to V_{CC} or GND.

NOTES: A. Resistor values shown are nominal.

B. Nonpolarized ceramic capacitors are acceptable. If polarized tantalum or electrolytic capacitors are used, they should be connected as shown.

00		
V _{CC}	C1	C2, C3, and C4
$\begin{array}{c} \textbf{3.3 V} \pm \textbf{0.3 V} \\ \textbf{5 V} \pm \textbf{0.5 V} \\ \textbf{3 V to 5.5 V} \end{array}$	0.1 μF 0.047 μF 0.1 μF	0.1 μF 0.33 μF 0.47 μF

V_{CC} vs CAPACITOR VALUES

Figure 5. Typical Operating Circuit and Capacitor Values

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
MAX3227ECDB	ACTIVE	SSOP	DB	16	80	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227ECDBG4	ACTIVE	SSOP	DB	16	80	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227ECDBR	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227ECDBRG4	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227EIDB	ACTIVE	SSOP	DB	16	80	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227EIDBG4	ACTIVE	SSOP	DB	16	80	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227EIDBR	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227EIDBRG4	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

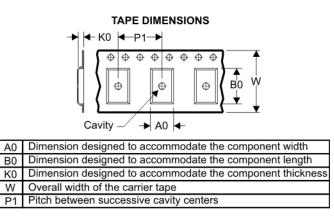
⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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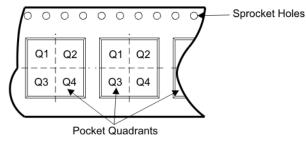
In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

TAPE AND REEL BOX INFORMATION





QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

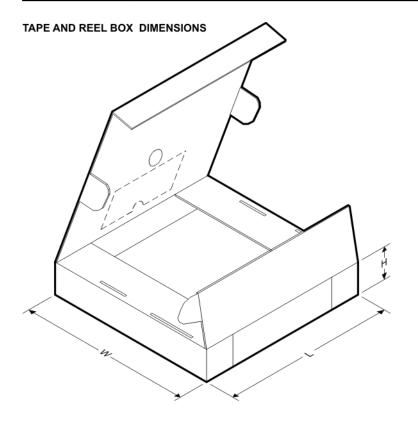


Device	Package	Pins	Site	Reel Diameter (mm)	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
MAX3227ECDBR	DB	16	SITE 41	330	16	8.2	6.6	2.5	12	16	Q1
MAX3227EIDBR	DB	16	SITE 41	330	16	8.2	6.6	2.5	12	16	Q1



PACKAGE MATERIALS INFORMATION

4-Oct-2007



Device	vice Package Pins Si		Site	Length (mm)	Width (mm)	Height (mm)	
MAX3227ECDBR	DB	16	SITE 41	346.0	346.0	33.0	
MAX3227EIDBR	DB	16	SITE 41	346.0	346.0	33.0	

MECHANICAL DATA

MSSO002E - JANUARY 1995 - REVISED DECEMBER 2001

DB (R-PDSO-G**)

PLASTIC SMALL-OUTLINE

28 PINS SHOWN



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
- D. Falls within JEDEC MO-150



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